

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,435,352 B2
APPLICATION NO. : 10/677182
DATED : October 14, 2008
INVENTOR(S) : J.-S. Mok et al.

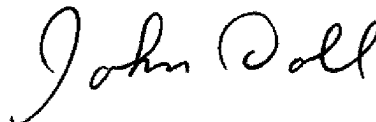
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>COLUMN</u>	<u>LINE</u>	<u>ERROR</u>
12 (Claim 14)	58	before "expose circuit patterns" delete "provide a solder resist pattern"

Signed and Sealed this

Fourth Day of August, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office